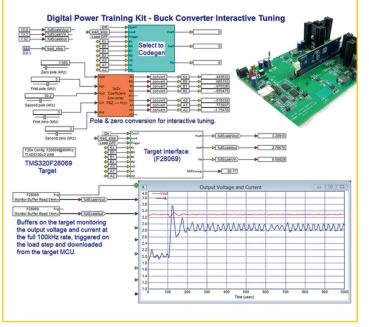
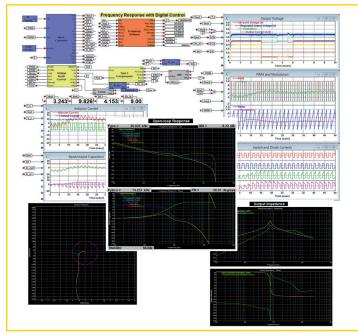
DATASHEET

Digital Power Designer™





Interactive tuning of the compensator poles and zeros while connected with the target hardware running as a hardware-in-loop simulation.



Model-based frequency response analysis of a digital control loop performed concurrently with the time-based simulation using the same pre-built, high-level simulation blocks

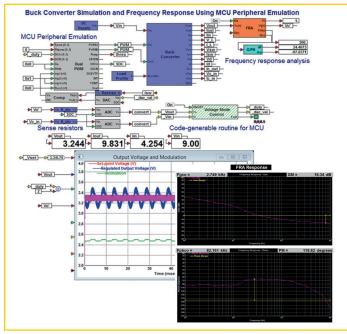
The **Digital Power Designer** is an add-on to Embed. Together with **Altair Embed**, it is aimed to be a complete tool for the design and development of digital power supplies and power conversion equipment.

"We have been successfully developing power conversion equipment with VisSim and the Digital Power Designer, now expanded in Embed, for more than 15 years and have been extremely satisfied with the performance and the ability to rapidly produce productionready designs directly from the Embed IDE, without the need to use a single line of C code."

Marco Donvito, R&D Department, CEG Elettronica Industriale S.p.A.

FEATURES

- Ready-made simulation blocks for complete simulation and verification of power converters within minutes.
- Set of code-generable blocks for use in simulations and for the creation of algorithms for automatic C code generation and downloading to the microcontroller hardware.
- Convert analog designs to digital designs.
- Perform model-based frequency response analysis and AC sweeps.
- Tools for compensator design.
- Debugging tools for TI C2000 microcontrollers.
- Easily create diagrams for the interactive verification and debugging of algorithms directly on the control hardware (processor-in-loop and hardware-in-loop).
- Rapid and simplified experimentation of control scenarios to assist in the optimal selection of digital and analog control techniques and configurations.
- Hundreds of worked examples demonstrating converter performance and for rapid development of power conversion systems including simulation, code generation and interactive hardware-in-Loop diagrams



AC sweep analysis of a digitally controlled converter performed directly on the simulation diagram using emulated microcontroller internal peripherals and the actual code-generable routine.

0.5@fx4.32		Veer (1%) DC CrLeff (4) VMC CrLeff (4) VMC CrLefe (4) Vie(1) (1%)	- (871) - (871) - (872)
	8035-EPWM1A/EPWM1B	F28x Config: F28035@60MHz TI XDS100v2 USB	
	280x ePWM Proper	ties	
PWM Unit: 1 V	Use High Res Timer		
Time Base Rate Scaling:	None V Count M	ode: Up/Down v	
Timer Period: 150		Change Period Dynamically	
TBCTR=TBPHS			
Change Phase D		Cl pin: GPIO6 v	
EPWMSYNCD: TE		CO pin: Unused v	
CMPA Load On: C1	R = Zero v CMPB Load	IOn: CTR =Zero V	
Action Qualifier:	CMPA CMPB Z up down up dow	m p GPIO Pin	
EPWMA:			
EPWMB:	X v 1 v 0 v X v X	✓ X ✓ GPI01 ✓	
Deadband			
Delay Mode:	Rising Edge Delay on DbAin&Fa	aling Edge Delay on DbBin 🔍 🗸	
Polarity: Input Select:	No Inversion	¥	
	DbA in = PWMA, Db8 in = PWI		
Rising Edge Delay (0	-1023): 64 Faling	Edge Delay (0-1023): 64	
Send Start ADC Cor	version Pulse A (SOCA): CT	R = PRD v /1 v	
Send Start ADC Cor	version Pulse B (SOCB)	CBEVT1 v /1 v	
Fault Handling			
EPwMA output on		V Digital Compare	
EPWMB output on	ault: High impedance (0 value forces Fault)	~	
One Shot TZx Fault			
CBC TZx Fault Sou		5 6 DCA DCB	
TZ1: GPI01			
TZ4:	TZ5:	1Z6: Y	

Ease of control loop creation using the digital power designer together with the Embed blocks. The diagram can be interactively run in the microcontroller RAM or flashed for final production.

CONTENT

Numerous high-level and completely configurable blocks including power converters (including buck, boost and buck-boost converters, multi-phase converters, Sepic, Cuk and Zeta converters, push-pull, half-bridge and full-bridge converters, forward and flyback converters, AC-DC converters, DC-AC Inverters and PFC converters), analog and digital compensators, power converter controller blocks (including voltage mode controllers, peak and average current mode controllers, PFC and hysteretic controllers), configurable PWM generators and controllers, analog and digital filters, sensor models and fully-configurable sources and loads. A set of simulation blocks for digital control simulations emulating TI C2000 MCU peripherals including ePWM, ADC, DAC, eCAP, comparators and GPIO, numerous code-generable blocks for digital control algorithm development, compensator design tools and tools for the conversion of analog to digital designs. Frequency response analysis tools. "A key benefit to using Embed is its code generation capability and how quickly and fast it works. The speed of how its drivers work and being able to interface directly with our ROM code was really the most important benefit."

Dave Wilson, Senior Motor Systems Engineer, Texas Instruments

APPLICATION AREAS

- Analog and digital power supplies.
- DC/DC converters.
- LED lighting.
- Electric vehicles.
- · Uninterruptible power supplies
- · Power conversion equipment.
- Power factor correction converters.
- Inverter design.
- Rectifiers and battery chargers.



For more information



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